



Transition from Hardware Requirements Checklist

Peer Review Version:
 Checklist Name:
 Document(s) Under Review:
 Supporting Documents:
 Reviewer Name:
 Reviewer Role:
 Notes:

#	Checklist Item (i.e., Question)	DO-254 or Other Guidance Reference	DAL				Latest Review Compliance	Latest Review Remarks	Compliance	Review Remarks		
			A	B	C	D	Y/N/NA	<add notes if there is something noteworthy about this review iteration>	Y/N/NA	<add notes if there is something noteworthy about this review iteration>		
			Review Date: <date>									
1	Has the T2 (Transition from Planning) review been satisfactorily completed with all items passing? (If not, do not proceed with this review)	NA (Best Practice)	X	X	X	X	0	0		0		
2	Have all appropriate Requirements Capture process artifacts been produced, baselined, reviewed, and placed under configuration management control (e.g., System Requirements, HRS, HRD, Traceability to System Requirements, Hardware CM Records, Hardware PA Records)?	NA (Best Practice)	X	X	X	X	0	0		0		
3	Have design constraints (due to production processes, standards, procedures, technology, design environment and design guidance) been identified?	5.1.2.3	X	X	X	X	0	0				
4	Have all hardware item requirements and derived requirements been developed?	5.1.1.1; 5.1.2.4	X	X	X	X	0	0				
5	Have all requirements (derived as well as those that are traceable to the upper-level requirements) been independently validated (especially with respect to safety) at some hierarchical level by review, analysis, or test?	6.1.2.2; AMC 20-152A CD-3	X	X			0	0				
6	Have all requirements (derived as well as those that are traceable to the upper-level requirements) been validated (especially with respect to safety) at some hierarchical level by review, analysis, or test?	6.1.2.2; AMC 20-152A CD-3			X	X	0	0				
7	Do the hardware requirements include the expected behavior of the design in response to abnormal and boundary conditions (i.e., Robustness Requirements)?	AMC 20-152A CD-8	X	X			0	0				
8	Has a requirements review been held to ensure the acceptability of requirements and compliance to the guidelines of 6.3.3.1?	6.3.3.1 8.1(2)	X	X	X	X	0	0				
9	Have derived requirements been provided to the System Safety Assessment Process?	5.1.1.2; 5.1.2.5; 6.1.2.3	X	X	X	X	0	0				
10	Are the requirements traceable to system requirements? Are derived requirements traceable as far as possible through the hierarchical levels?	5.1.2.8	X	X	X	X	0	0				
11	Are the requirements complete and compliant with respect to system requirements?	6.1.2.4; 6.1.2.5	X	X	X	X	0	0				
12	Has evidence been established between the derived and hardware requirements and the validation activities and results? (This is typically part of a validation record in the trace matrix or peer review records.)	6.1.2.6	X	X	X	X	0	0				
13	If COTS IP is used, do the requirements include all the IP functions, including unused ones for deactivation?	AMC 20-152A IP-6	X	X	X		0	0				

14	Have the planned transition criteria for entering the Hardware Conceptual Design Process as identified in the HDP been satisfied?	3.2 8.1(1)(2) 8.2(4)	X	X	X	X	0	0				
15	Have requirement omissions and errors been provided to the appropriate process for resolution?	5.1.1.3; 5.1.2.7; 6.1.2.7	X	X	X	X	0	0				

0	Total "Y"	0	Total "Y"
0	Total "N"	0	Total "N"
0	Total "NA"	0	Total "NA"
0	Total "0"	0	Total "0"
0	0	0	0